

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,975,665 B1
APPLICATION NO. : 09/685199
DATED : December 13, 2005
INVENTOR(S) : John W. McCorkle

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 24, Line 44, Claim No. 1:

Change "signal in frequency;" to --signal in frequency,--

In Column 24, Line 46, Claim No. 1:

Change "a plurality of high frequency clock signals;" to --a plurality of high frequency clock signals,--

In Column 24, Line 48, Claim No. 1:

Change "plurality of low frequency control signals;" to --plurality of low frequency control signals,--

In Column 25, Line 55, Claim No. 9:

Change "frequency clock signals;" to --frequency clock signals,--

In Column 25, Line 59, Claim No. 9:

Change "the agile timing signal in frequency; and" to --the agile timing signal in frequency, and--

In Column 27, Line 16, Claim No. 20:

Change "modulating the agile timing signal in frequency base on a" to --modulating the agile timing signal in frequency based on a--

In Column 28, Line 12, Claim No. 25:

Change "high frequency clock signals;" to --high frequency clock signals,--

In Column 29, Line 30, Claim No. 33:

Change "generation circuit;" to --generation circuit,--

In Column 29, Line 32, Claim No. 33:

Change "high frequency clock signals;" to --high frequency clock signals,--

In Column 29, Line 34, Claim No. 33:

Change "low frequency clock signals;" to --low frequency clock signals,--

In Column 29, Line 43, Claim No. 33:

Change "signals via the controller; and" to --signals via the controller, and--

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 29, Line 60, Claim No. 34:

Change "generation circuit for the tuning" to --generation circuit for fine tuning--

In Column 30, Line 57, Claim No. 41:

Change "signals via the control means;" to --signals via the control means,--

In Column 30, Line(s) 62-63, Claim No. 41:

Change "phase relationships respectively such that the first" to --phase relationships, respectively, such that the first--

In Column 32, Line 4, Claim No. 49:

Change "clock signals via the controller;" to --clock signals via the controller,--

In Column 33, Line 31, Claim No. 57:

Change "tuning of the agile signal in frequency" to --tuning of the agile timing signal in frequency--

In Column 34, Line 47, Claim No. 65:

Change "generation circuit;" to --generation circuit,--

In Column 34, Line 49, Claim No. 65:

Change "high frequency clock signals;" to --high frequency clock signals,--

In Column 34, Line 51, Claim No. 65:

Change "low frequency clock signals; and" to --low frequency clock signals, and--

In Column 34, Line 60, Claim No. 65:

Change "controller;" to --controller,--

In Column 34, Line 61 Claim No. 65:

Change "wherein the low frequency con generation" to --wherein the low frequency control generation--

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In Column 35, Line 3, Claim No. 66:

Change "of the agile-timing signal in frequency." to --of the agile timing signal in frequency.--

In Column 36, Line 8, Claim No. 73:

Change "signals via the control means;" to --signals via the control means,--

In Column 37, Line 24, Claim No. 81:

Change "frequency clock signals via the controlled;" to --frequency clock signals via the controller,--

In Column 38, Line 48, Claim No. 89:

Change "via the controller; and" to --via the controller, and--

Signed and Sealed this

Twenty-second Day of July, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office